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Docket No.: L&L-10217

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By: [Signature] Date: February 22, 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No. : 10/620,093 Confirmation No: 8716  
Applicant : Jörg Berthold et al.  
Filed : July 15, 2003  
Art Unit : 2825  
Examiner : Phallaka Kik  
Title : Method for Determining the Critical Path of an Integrated Circuit  
Docket No. : L&L-10217  
Customer No. : 24131

**LETTER**

Mail Stop: Missing Parts  
Hon. Commissioner for Patents,  
Alexandria, VA 22313-1450

Sir:

Enclosed herewith is the signed substitute declaration for the above mentioned non-provisional patent application.

Respectfully submitted,

[Signature]

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REG. NO. 37,237

/mjb  
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